

# Application of a Power Electronic Switch in a Vacuum Installation

Ruslan Ivanov<sup>1</sup>, Hristo Antchev<sup>2</sup>,  
Mintcho Mintchev<sup>3</sup>, Mihail Antchev<sup>4</sup>

**Abstract:** This article proposes an original method of implementing a power electronic semiconductor switch using a technological process based on metal processing in a vacuum furnace. We give schematic diagrams of the power part and the control system, and oscillograms for their operation. Results from a practical application and experimental research on the operation of the vacuum installation are presented, and the variation in the glow discharge current with and without operation of the power electronic switch is explored. Photographs showing the details of the machining with and without the power electronic switch are included to demonstrate the effective operation of the proposed schematic solution.

**Keywords:** Power electronic switch, Vacuum furnace, Control system.

## 1 Introduction

The basic principles of vacuum technology and its potential have been reviewed in [1]. Glow discharge is widely used in the modern vacuum heat treatment of metals. Preliminary cleaning and activation of the surface for subsequent heat treatment or coating are carried out, and during these processes, the probability of the normal glow discharge transitioning into an arc discharge is high. As a result, defects on the surface of the details occur, which can lead to low quality and an increase in production waste, meaning that it is necessary to take measures to prevent the appearance of arc discharge.

There are several possible solutions based on the following process of operation: (i) the spontaneous transition of glow discharge into arc discharge is detected; (ii) the current is briefly reduced to zero to extinguish the arc; (iii) following this, the glow discharge is restored [2–4]. The main disadvantage of this process is that it does not prevent the occurrence of an arc discharge, and

---

<sup>1</sup>Technovacsystem Ltd., Rousse, Bulgaria, office@technovacsystem.com

<sup>2</sup>University of Chemical Technology and Metallurgy, Department of Metallurgical Technologies, Electrical Engineering and Electronics, Sofia, Bulgaria; E-mail: hristo\_antchev@uctm.edu

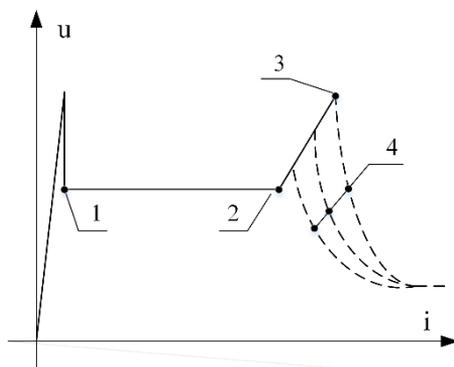
<sup>3</sup>Technical University-Sofia, Department Electrical apparatus, Sofia, Bulgaria; E-mail: mintchev@tu-sofia.bg

<sup>4</sup>Technical University-Sofia, Department Power electronics, Sofia, Bulgaria; E-mail: antchev@tu-sofia.bg

during the time it takes place, defects on the surface of the details can occur. Spontaneous transition of the glow discharge to an arc can be prevented by applying a pulsed power supply.

The choice of a scheme for supplying a pulsed power supply for glow discharge and the selection of an algorithm for its control are mainly determined by two factors:

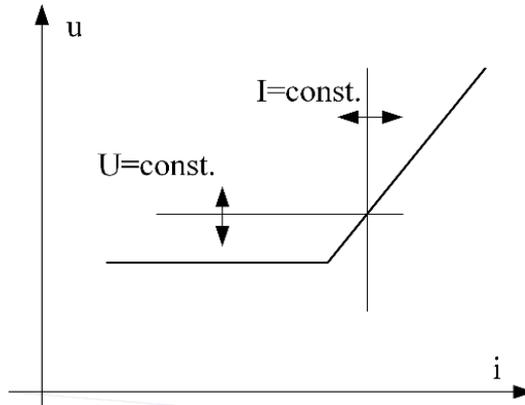
- a) The peculiarities of maintaining a glow discharge at high power value. At surface treatment of details in glow discharge, the operation must be in the area of the abnormal glow discharge. This is necessary so the entire surface of the details is covered by the gas discharge.
- b) When operating in the abnormal glow discharge range, a spontaneous transition of the glow discharge into an arc occurs, as illustrated in Fig. 1.



**Fig. 1** – Transition of the glow discharge into arc discharge:  
 $u$  – discharge voltage;  $i$  – discharge current.

The range of normal glow discharge occurs between the points marked 1 and 2. The discharge voltage in this range remains constant as the current changes, and the current density on the cathode is also constant. The discharge covers part of the cathode surface. It is only at point 2 that the entire cathode is covered by the glowing discharge. The abnormal glow discharge is characterised by a voltage that increases with the current. This occurs between points 2 and 3 in Fig. 1. With points 4, a spontaneous transition from glow to arc discharge is illustrated. This transition depends on a large number of factors, and is observed at different operating points within the range of abnormal glow discharge. The arc discharge damages the surfaces of the samples and must be extinguished as soon as possible. The measures that can be used to decrease the probability of the glow discharge turning into an arc are limited to either decreasing the intensity of the discharge, which is disadvantageous, or providing a pulsed power supply, which can achieve the necessary conditions without reducing the intensity of operation.

The operating point corresponding to the abnormal glow discharge can be supported by both a source that sets a constant voltage (i.e., a voltage source) and by a source that sets a constant current (a current source), as illustrated in Fig. 2. When it is necessary to operate at the border between normal and abnormal glow discharge, a voltage source cannot ensure stable operation; however, a current source can provide a stable operating point over the entire range of possible variations of the operating points in both the abnormal and normal glow discharge ranges.



**Fig. 2** – Operation with a current source and a voltage source.

When powered by a voltage source, as the glow discharge spontaneously turns into an arc, the current will rise inadmissibly. There is a high probability that local defects in the details will occur until the short circuit protection is triggered.

When powered by a current source, however, the current remains unchanged even if the system switches to arc discharge; this reduces the risk of causing defects at the surface of the samples, meaning that it is not necessary to resort to triggering the short-circuit protection. The following procedure is typically used. The spontaneous transition of the glow discharge into an arc is detected, and the current is briefly reduced to zero in order to extinguish the arc discharge, after which the glow discharge is restored. A pulsed power supply can be created by connecting a transistor short circuit semiconductor switch in parallel with the working chamber to interrupt the glow discharge. The control of the semiconductor switch enables periodic switching on and off, with a certain frequency and duration. The duration of the switched-off state should be set depending on the speed of deionisation of the discharge gap after an arc discharge has occurred and been extinguished, and the frequency of repetition of the interruptions is determined depending on the speed of the transition from glow to arc discharge.

Both processes are influenced by many factors, the most significant of which are the dimensions of the camera, the pressure and temperature at which the technological process is carried out, and the characteristics of the samples (such as their quantity, arrangement, shape and the cleanliness of their surfaces) [3].

In view of the above, the scheme chosen for the switch mode power supply should provide options for different settings of the frequency and duration of the interruption, to meet the needs of different technologies. According to preliminary estimates, the duration of the interruption should be in the range [10, 100]  $\mu\text{s}$ , and the repetition frequency should not exceed (10–12) kHz.

In addition to the need to adjust the frequency and duty cycle of the control pulses of the power semiconductor switch, there are also requirements for reliable operation and electromagnetic compatibility under strong disturbing effects and operation at high voltages.

Existing methods of implementing power electronic switches using different power semiconductor elements are discussed and systematised in [5–7]; for example, power thyristor switches can be used in electronic overvoltage protection circuits [8]. A DC thyristor switch is reviewed in [9]. In [10], the authors describe a bidirectional DC thyristor switch based on a G-source. The authors of [11] studied the operation of parallel-connected electronic DC switches, each of which consisted of two injection-enhanced gate transistors connected with each other in parallel along the diagonal of a single-phase diode bridge circuit. The DC switching circuit was connected along the other diagonal. A combination of parallel- and series-connected insulated gate bipolar transistors (IGBTs) for the implementation of a DC switch is discussed in [12]. A switch for DC networks is described in [13] based on a combination of a power transistor and inductances with mutual connection. In [14], a switch for low voltage DC networks based on IGBT is described.

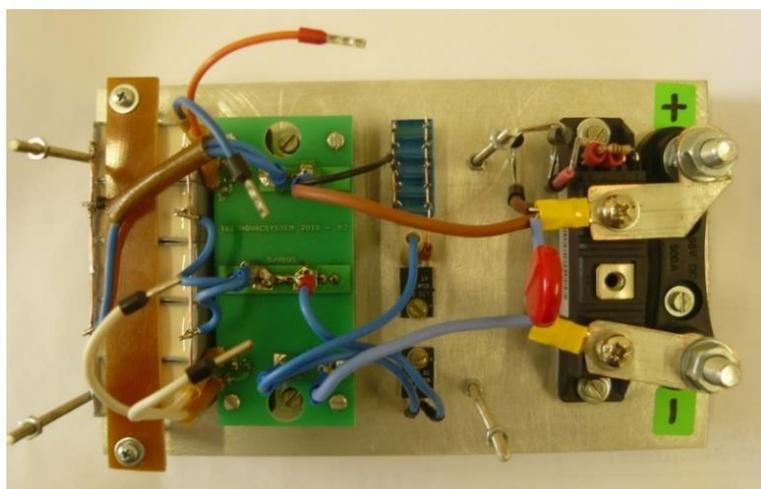
The purpose of the switch considered in this research is more specific than those considered in the studies described above, due to the requirement for a continuous interruption to the current with a certain frequency in a high-voltage DC circuit. To meet these requirements, the selection of a schematic solution, of elements and the practical implementation of a power electronic switch need to be made by researchers and designers with a great deal of knowledge and experience. This is due to the requirements for reliable operation, electromagnetic compatibility with strong disturbing effects and operation at high voltages.

## **2 Power Scheme and System for the Control of a Power Electronic Switch**

Fig. 3a shows a block diagram of the proposed vacuum installation. Its elements are as follows: 1 – three-phase power transformer; 2 – three-phase



transitional processes of switching. The features of the protective elements used in transistor switches for DC circuits with inductive load are discussed in [16–18], and their influence on the switching process is explained. In the present work, for protective purposes, we use an RCD group R1C2D1, an RC group R1C1, and an input varistor RV1. These three measures provide effective protection against operational overvoltages. The RCD group R1C2D1 controls the movement of the operating point when switching the transistor along a trajectory in the safe operating area. The RC group R1C1 and the varistor RV1 limit the switching overvoltages. In addition, for protection against extreme inadmissible overvoltage between the input terminals, the thyristor Q2 is used, together with the elements around it (R3, R4, D2, D3, D4 and D5). These are connected according to a short-circuit scheme, and in the event of an accidental increase in voltage outside the operating limits, the thyristor Q2 will be turned on, which will cause the automatic circuit-breaker of the supply voltage to switch off. The NTC terminals are connected to an internal module thermistor with a value of 50 k $\Omega$  at temperature 25°C, which is included in the scheme of the control system and is used to monitor the temperature of the module. The installation of the elements of the power scheme for a cooling radiator with natural air cooling is shown in Fig. 4. This scheme meets the requirements in terms of ensuring the necessary insulation voltages and minimising parasitic inductances.

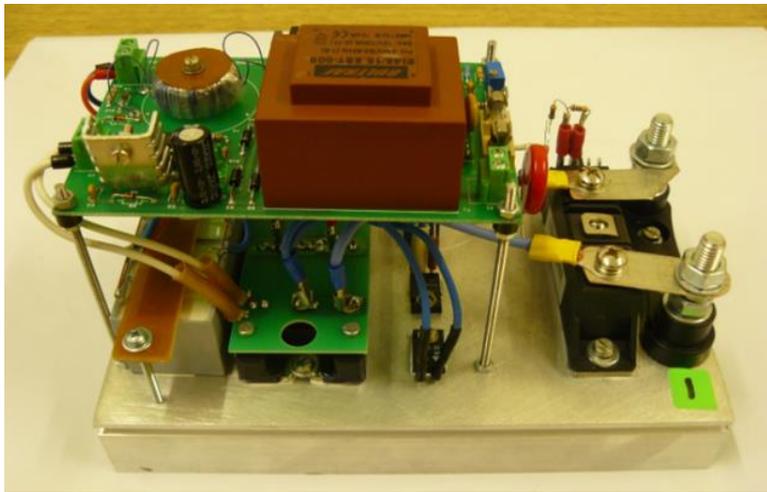


**Fig. 4** – *Elements of the power circuit installed on a cooling radiator.*

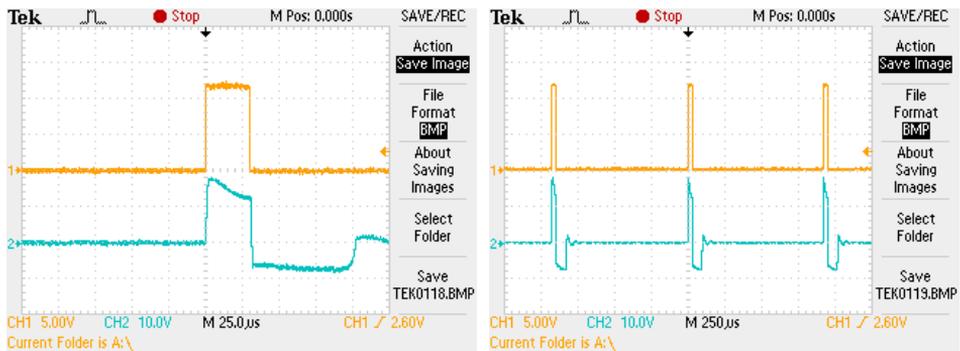
Fig. 5 shows a schematic diagram of the control system. Its main element is the multivibrator in autogenerator mode, which is implemented with an integral circuit U1. By means of the potentiometer R3, the duration of the output pulse at



lower than the equipment from the aforementioned companies. The IGBT control driver is implemented with all elements connected to the right of terminal 3 of U1 at the bottom of the circuit. The circuit is optimised for the operating conditions described above with all necessary measures taken to ensure the shape of the control pulse. Galvanic separation is achieved with a high-frequency transformer at an isolation voltage of 3 kV. Fig. 6 shows the implementation of the entire power electronic switch. Oscillograms of the control pulses are shown in Fig. 7 for an operating frequency of 1 kHz and a duration of the control pulse of 30  $\mu$ s. The switch-on voltage for the IGBT is about 10 V, and the switch-off voltage is about -5V.



**Fig. 6** – External appearance of the entire power electronic switch with the control system.



**Fig. 7** – Oscillograms of the control pulses: CH1 – at the anode of diode D8; CH2 – voltage G-E of IGBT.

### 3 Experimental Research

In order to determine the effectiveness of the power switch, it was implemented in a vacuum furnace used for ion nitriding, carbonitriding, and nitrocementation, made by Technovacsystem Ltd., as shown in Fig. 8 [23]. Samples of the same shape and size were precleaned of the coolant required for machine treatment, and were dried. They were divided into two groups: the first set were cleaned under glow discharge conditions, without the use of the power electronic switch, and the second were cleaned with the operation of the electronic switch.



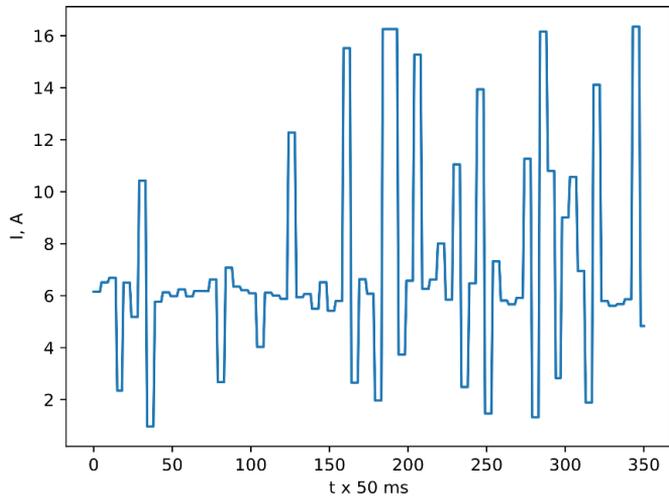
**Fig. 8** – *Vacuum furnace for thermal and chemical-thermal treatment of metals with pulse glow discharge.*

The frequency and duty cycle of the pulses generated by the power switch were determined based on the results of the previous experiments. Values of  $f = 8$  kHz and duty cycle  $D = 0.2$  were defined to ensure optimal operation of the glow discharge in the existing setup.

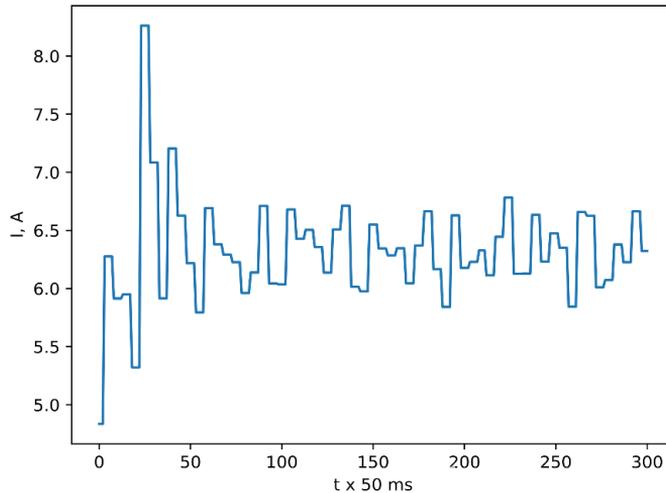
The details were loaded into the vacuum furnace. By means of a vacuum pump, the pressure of the working space of the furnace was set to 2 mbar. The glow discharge current was applied, and a program for recording the changes in this current throughout the process was started.

Numerous experiments were carried out. Given the large amount of data obtained and the impossibility of presenting all of the information, only a selection of results are presented in Fig. 9. It can be seen that in cases when the power electronic switch was not operational, the conditions for a repeated increase in the discharge current were created, leading to a transition of the glow discharge into an arc. The occurrence of such a discharge is shown in Fig. 10. As a result of this discharge, defects appear on the surface of the samples.

Fig. 11 shows the external appearance of details from both groups. On the left is an example from the group processed without turning on the power electronic switch, with the occurrence of an arc discharge, and on the right is an example from the group where this discharge was prevented through the use of the power electronic switch.

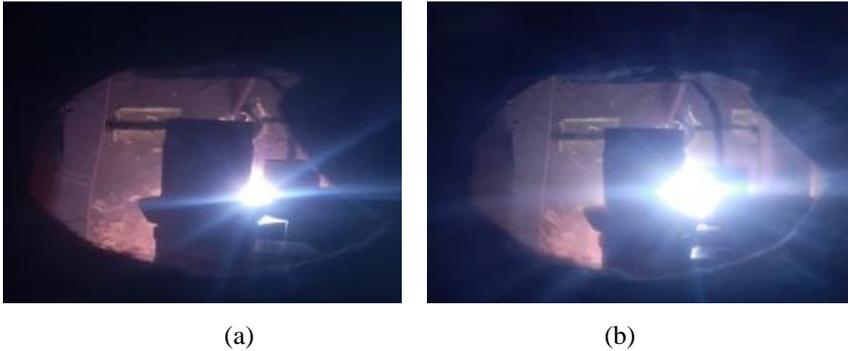


(a)



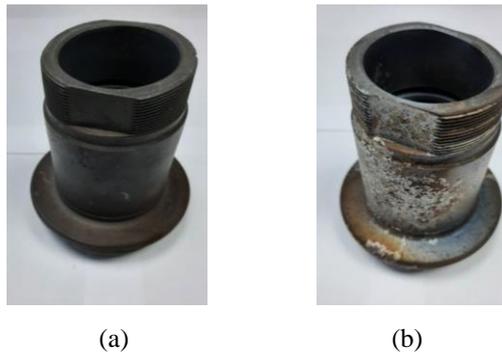
(b)

**Fig. 9** – Changes in glow discharge current: (a) without power switch; (b) with power switch.



**Fig. 10** – Arc discharge during processing of the details in glow discharge without operation of the power switch.

From Fig. 11a, the defects on the surface of the detail on the left as a result of the action of the arc discharge can be clearly seen.

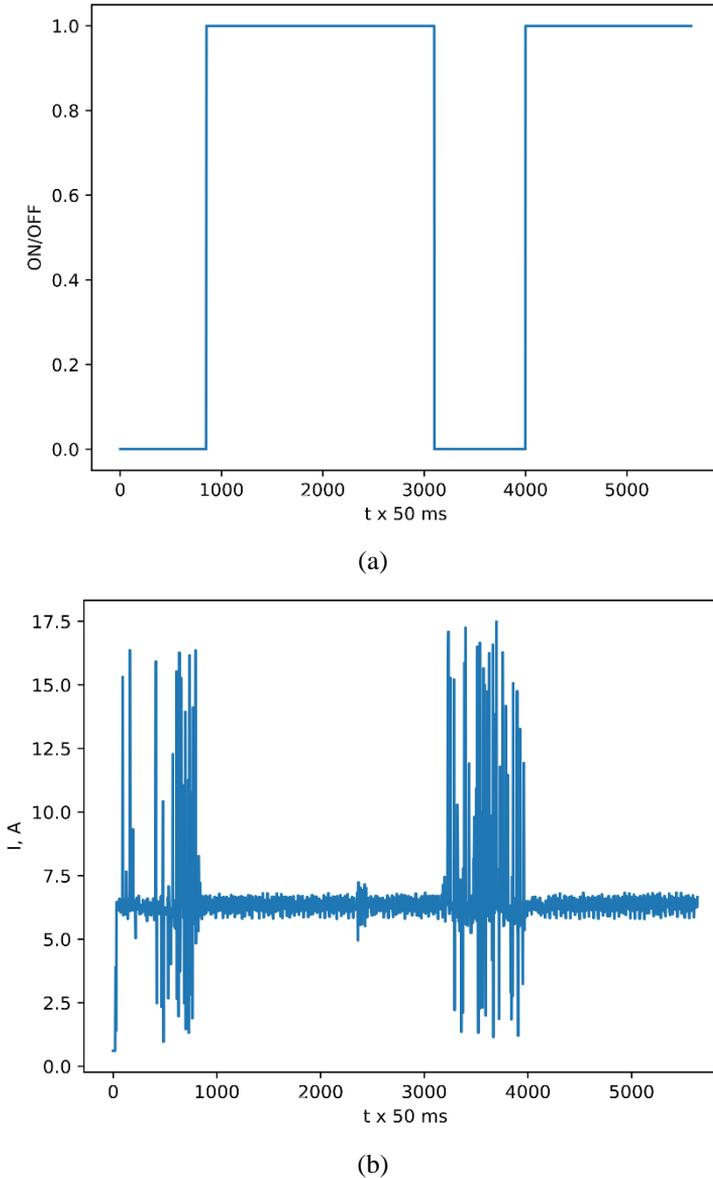


**Fig. 11** – External appearance of details: (a) processed with power electronic switch; (b) processed without power electronic switch.

Additional experiments were carried out in which the details were placed in the vacuum furnace and processed under glow discharge conditions, with the same parameters as in the previous experiments. A program was written and loaded into the industrial computer to control the furnace, which generated operation times of random duration. This permitted the power electronic switch to be switched on or off at any time, and for a randomly selected duration. The shape of the signal used to enable the power switch is shown in Fig.12a. During the high level (logical 1), the operation of the power electronic switch is permitted. The variation in the glow discharge current is shown in Fig.12b.

From Figs. 12a and 12b, it can be seen that during the operation of the power electronic switch, the conditions necessary for an increase in the concentration of ions in the inter-electrode space and higher discharge current are not provided.

In most cases, in the event of an arc discharge, when the power electronic switch is turned on, the discharge was interrupted and the glow discharge was restored. Operation with power electronic switch and maintaining this discharge is presented in Fig. 13.



**Fig. 12** – (a) Control signal to the power electronic switch;  
(b) Change in the glow discharge current.



**Fig. 13** – *Glow discharge with operation of the power electronic switch.*

## 4 Conclusion

In this article, we have presented an original method of implementing a power electronic semiconductor switch for metal processing in a vacuum furnace. The results of our experiments show a reduction in the glow discharge current variations when the power switch is used. From an analysis of these results, the following conclusions can be drawn:

- The efficiency of the process of cleaning the details in glow discharge increases with the use of a power semiconductor switch.
- The probability of occurrence of an arc discharge is significantly reduced.
- The probability of defects occurring on the surface of the details is decreased, and the creation of waste is reduced.

## References

- [1] Festo: Basic Principles of Vacuum Technology, Brief Overview, Available at: [https://www.festo.com/net/SupportPortal/Files/286804/Basic\\_Vacuum\\_Technology\\_Principles.pdf](https://www.festo.com/net/SupportPortal/Files/286804/Basic_Vacuum_Technology_Principles.pdf)
- [2] J. L. Dossett, G. E. Totten: Steel Heat Treating Fundamentals and Processes, ASM Handbook, Vol. 4A, ASM International, Materials Park, 2013.
- [3] Д. Т. Бучков, В. Ц. Тошков: Йонно азотиране, Техника, София, 1989.
- [4] Ю. М. Лахтин, А. Ф. Коган: Азотирование стали. Машиностроение, Москва, 1976.
- [5] I. N. Jiya, R. Gouws: Overview of Power Electronic Switches: A Summary of the Past, State-of-the-Art and Illumination of the Future, Micromachines, Vol. 11, No. 12, December 2020, p. 1116.
- [6] C. Gu, P. Wheeler, A. Castellazzi, A. J. Watson, F. Effah: Semiconductor Devices in Solid-State/Hybrid Circuit Breakers: Current Status and Future Trends, Energies, Vol. 10, No. 4, April 2017, p. 495.

- [7] J. He, K. Chen, M. Li, Y. Luo, C. Liang, Y. Xu: Review of Protection and Fault Handling for a Flexible DC Grid, Protection and Control of Modern Power Systems, Vol. 5, No. 2, April 2020, pp. 1 – 15.
- [8] K. Milanov, H. Antchev: Fast-Acting Protection of a Semiconductor Converter by Means of Short-Circuiting Thyristor, Proceedings of the 10<sup>th</sup> Electrical Engineering Faculty Conference (BulEF), Sozopol, Bulgaria, September 2018, pp. 1 – 5.
- [9] J.- Y. Kim, S.- S. Choi, I.- D. Kim: A New Reclosing and Re-Breaking DC Thyristor Circuit Breaker for DC Distribution Applications, Journal of Power Electronics, Vol. 17, No. 1, January 2017, pp. 272 – 281.
- [10] J. Gao, Z. Liu, J. Luo, H. Wang, Y. Liu, J. Zheng: A Bi-Directional DC Solid-State Circuit Breaker Based on Flipped  $\Gamma$ -Source, Energies, Vol. 15, No. 3, January 2022, p. 745.
- [11] L. Qu, Z. Yu, R. Zeng, X. Yan, Z. Gan, Y. Huang: Parallel Breaking Characteristics of Diode-Bridge Power Electronic Switch for DC Circuit Breaker, International Journal of Electrical Power & Energy Systems, Vol. 138, June 2022, p. 107929.
- [12] J. Xi, X. Pei, L. Niu, T. Feehally, P. Wilson, C. Gu, X. Zeng: A Solid-State Circuit Breaker for DC System Using Series and Parallel Connected IGBTs, International Journal of Electrical Power & Energy Systems, Vol. 139, July 2022, p. 107996.
- [13] J.- M. Park, H.- J. Byun, S.- H. Kim, S.- H. Kim, C.- Y. Won: DC Solid-State Circuit Breakers with Two-Winding Coupled Inductor for DC Microgrid, Energies, Vol. 14, No. 14, July 2021, p. 4129.
- [14] L. Tapia, I. Baraia-Etxaburu, J. J. Valera, A. Sanchez-Ruiz, G. Abad: Design of a Solid-State Circuit Breaker for a DC Grid-Based Vessel Power System, Electronics, Vol. 8, No. 9, August 2019, p. 953.
- [15] Microsemi: APTGT100SK170TG Datasheet, Available at: <https://www.microchip.com>.
- [16] Y. Wu, Q. Yi, Y. Wu, F. Yang, Z. Zhang, J. Wen: Research on Snubber Circuits for Power Electronic Switch in DC Current Breaking, Proceedings of the 14<sup>th</sup> IEEE Conference on Industrial Electronics and Applications (ICIEA), Xi'an, China, June 2019, pp. 2082 – 2086.
- [17] Q. Yi, F. Yang, Y. Wu, Y. Wu, C. Gao, W. Zhuang, M. Rong: Snubber and Metal Oxide Varistor Optimization Design of Modular IGCT Switch for Overvoltage Suppression in Hybrid DC Circuit Breaker, IEEE Journal of Emerging and Selected Topics in Power Electronics, Vol. 9, No. 4, August 2021, pp. 4126 – 4136.
- [18] Z. Wang, E. M. Sankara Narayanan: Design of a Snubber Circuit for Low Voltage DC Solid-State Circuit Breakers, IET Power Electronics, Vol. 14, No. 6, May 2021, pp. 1111 – 1120.
- [19] L. Balogh: Fundamentals of MOSFET and IGBT Gate Driver Circuits, Application Report – SLUA618A, Texas Instruments, Revised 2018.
- [20] H. Kim: Gate Drive Controller for Low Voltage DC Hybrid Circuit Breaker, Energies, Vol. 14, No. 6, March 2021, p. 1753.
- [21] Ultra-High CMTI Isolated Gate Drivers, Analog Devices, Inc., Revised 2022, Available at: <https://www.analog.com/media/en/technical-documentation/data-sheets/max22700d-max22702e.pdf>.
- [22] Power Integrations: Application with SCALE-2 and SCALE-2+ Gate Driver Cores, Application note 1101, Available at: <https://www.power.com/design-support/application-notes>.
- [23] Technovacs system, Available at: [www.technovacs.com](http://www.technovacs.com)